

### **REMARKS**

Claims 1, 2, and 4-10 are presently pending and stand rejected.

Claims 1, 2, and 4 were rejected under 35 U.S.C. § 103(a) as being obvious from the combination of Fimoff in view of Yuan.

Claim 1 is amended to recite, among other limitations, "said pixel reconstructor further comprising: a macroblock input buffer for storing the reference pixels that are fetched from a frame buffer by the video request manager at the addresses calculated by the motion vector address computer; and a register for storing a portion of the reference pixels that are fetched from the frame buffer by the video request manager at the addresses calculated by the motion vector address computer."

Assignee respectfully submits that the combination of Fimoff and Yuan do not teach the foregoing limitation. Although Examiner has indicated that Yuan and Fimoff "as a whole further teaches wherein pixel reconstructor (Yuan, fig. 8, 800) comprises: a macroblock input buffer for storing the reference pixels (Yuan; fig. 8, 801 frame input buffer. Further, the frame input buffer stores both past and future reference frames, which contain reference pixels); and a register for storing a portion of the reference pixel (Yuan, fig. 8, 801. The frame buffer stores both past and future reference frames, which would inherently contain reference pixels)", Office Action at 5, Assignee respectfully submit that the foregoing do not teach "for storing the reference pixels that are fetched from a frame buffer by the video request manager at the addresses calculated by the motion vector address

computer".

It is noted that Yuan 801, Frame Buffer stores the entire frames, and clearly would not be "the reference pixels that are fetched from a frame buffer by the video request manager at the addresses calculated by the motion vector address computer".

Accordingly, Assignee respectfully requests that Examiner withdraw the rejection to claim 1, 2, and 4.

Claims 5-10 were rejected under 35 U.S.C. 103(a) as being obvious from the combination of Diaz in view of Moon.

Claim 5 is amended to recite, among other limitations, "a macroblock input buffer for storing reference pixels that are referenced by at least one motion vector". Although Examiner has indicated that Diaz teaches "a macroblock input buffer (FIFO, fig. 3)", Assignee respectfully submits that the FIFO in Diaz Figure 3 does is not "storing reference pixels that are reference by at least one motion vector".

Accordingly, Assignee respectfully requests that Examiner withdraw the rejection to claims 5-10.

### **CONCLUSION**

For at least the foregoing reasons, Assignee respectfully submits that each of the pending claims are allowable and Examiner is respectfully requested to pass this case to issuance. The Commissioner is hereby authorized to charge additional fees or credit overpayments

to the deposit account of McAndrews, Held & Malloy, Account  
No. 13-0017.

Dated: September 15, 2008

Respectfully submitted,

A handwritten signature in dark ink, appearing to read 'Mirut Dalal', written over a horizontal line.

Mirut Dalal

Reg. No. 44,052

Attorney for Applicants

McAndrews, Held & Malloy, Ltd.  
500 West Madison Street  
Chicago, Illinois 60661

Telephone: (312) 775-8000

Facsimile: (312) 775-8100